PATENT Customer No. 22,852 Attorney Docket No. 09894.0005-00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: Unassigned
Examiner: Unassigned
Confirmation No.: 1701
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Alexandria, VA 22313-1450

Sir:

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents on the attached listing. This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits for the above-referenced application.

The following documents were cited in a communication for the related PCT International application (PCT/CH2004/000258) to this U.S. National Stage application: WO 03/017482 A1, "Temperature compensation of silicon resonant pressure sensor," "Design of resonators for the determination of the temperature coefficients of elastic constants of monocrystalline silicon," and "A micromachined, single-crystal silicon, tunable resonator." A copy of the International Search Report for PCT/CH2004/000258

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is attached. A copy of the listed foreign patent document, as well as copies of the nonpatent literature documents are also attached.

In addition, the following documents were cited in a communication for the related PCT International application (PCT/CH2004/000288) which is relevant to copending and commonly assigned U.S. Application No. 10/556,909: U.S. Patent No. 3,826,931, JP 55-112043, "Low Power Timekeeping," "The Microcomputer Compensated Crystal Oscillator (MCXO)", and "A Microprocessor-Based Analog Wristwatch Chip with 3 Seconds/Year Accuracy." A copy of the International Search Report for PCT/CH2004/000288 is attached. A copy of the listed foreign patent document, as well as copies of the non-patent literature documents are also attached. However, a copy of the cited U.S. patent is not enclosed.

Applicant respectfully requests that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and Applicant determines that the cited documents do not constitute "prior art" under United States law, Applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents.

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Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: March 7, 2006

Leila R

Reg. No. 52,399

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IDS Form PTO/SB/08: Substitute for form 1449A/PTO  INFORMATION DISCLOSURE STATEMENT BY APPLICANT		C	omplete if Known			
		Application Number	10/556,831			
		RE	Filing Date	November 15, 2005		
		First Named Inventor	Claude BOURGEOIS			
STATEMENT OF APPLICANT			14.1	Art Unit	Unassigned	
(Use as many sheets as necessary)				Examiner Name	Unassigned	
Sheet	1	of	1	Attorney Docket Number	09894.0005-00	

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS						
Cite	Document Number	Issue or	Name of Patentee or	Pages, Columns, Lines, Where		
No.' Number-Kind Code <sup>2</sup> (if	No.'	Number-Kind Code <sup>2</sup> (if known)	Publication Date Applicant of Cited Document  wn) MM-DD-YYYY			Relevant Passages or Relevant Figures Appear
_	US-3,826,931	07-30-1974	Hammond			
	Cite No. <sup>1</sup>	Cite Document Number  No. 1 Number-Kind Code <sup>2</sup> (if known)	Cite No.1 Number-Kind Code2 (if known) Issue or Publication Date MM-DD-YYYY	Cite No.1 Number-Kind Code <sup>2</sup> (if known) Issue or Publication Date MM-DD-YYYY		

Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation <sup>6</sup>
		WO 03/017482 A1	02-27-2003	The Regents of the University of Michigan		
		JP 55-112043	08-29-1980	Seiko Instr. & Electronics Ltd.		Abstract

	NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation <sup>6</sup>			
		Veis J., "Temperature compensation of silicon resonant pressure sensor," Sensors and Actuators A, Elsevier Sequoia S.A., Lausanne, CH, Vol. 57, No. 3, December 1, 1996, pages 179-182.				
		C. Bourgeois et al., "Design of resonators for the determination of the temperature coefficients of elastic constants of monocrystalline silicon," 1997 IEEE International Frequency Control Symposium, Orlando, FL, May 28-30, 1997, New York, New York, May 28, 1997, pages 791-799.				
		J. J. Yao, N. C. MacDonald, "A micromachined, single-crystal silicon, tunable resonator," J. Micromech. Microeng., Vol. 5, No. 3, May 28, 1997, pages 257-264.				
		Bloch, Martin et al., "Low Power Timekeeping," 43 <sup>rd</sup> Annual Symposium on Frequency Control, May 31, 1989, pages 34-36.				
		Bloch, Martin et al., "The Microcomputer Compensated Crystal Oscillator (MCXO)," 43rd Annual Symposium on Frequency Control, May 31, 1989, pages 16-19.				
		Lanfranchi, Didier et al., "A Microprocessor-Based Analog Wristwatch Chip with 3 Seconds/Year Accuracy," IEEE International Solid State Circuits Conference, IEEE, Inc., New York, Vol. 37, February 1, 1994, pages 92-93.				
		U.S. Patent Application No. 10/556,909, filed November 15, 2005.				
		Copy of International Search Report, (PCT/CH2004/000258) mailed August 20, 2004 (3 pages).				
		Copy of International Search Report, (PCT/CH2004/000288) mailed August 20, 2004 (3 pages).				

Examiner	 Date	
Signature	 Considered_	